

000221" E2054260

APPROVED FOR PUBLICATION
CLASS SUBCLASS
57
EMPLOYEE

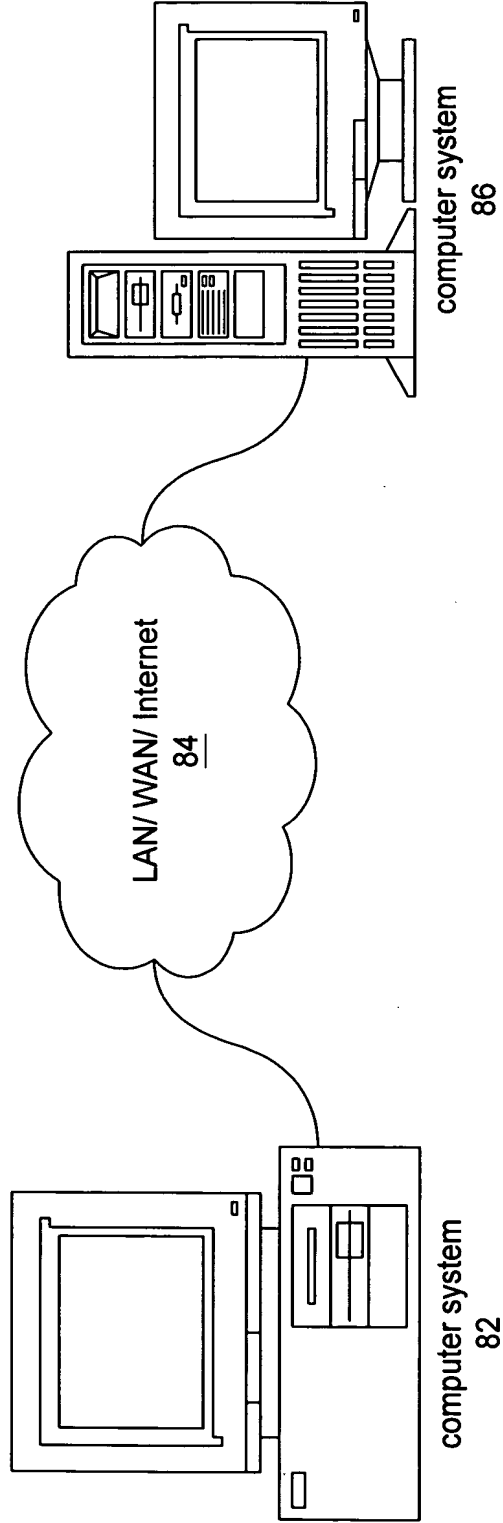
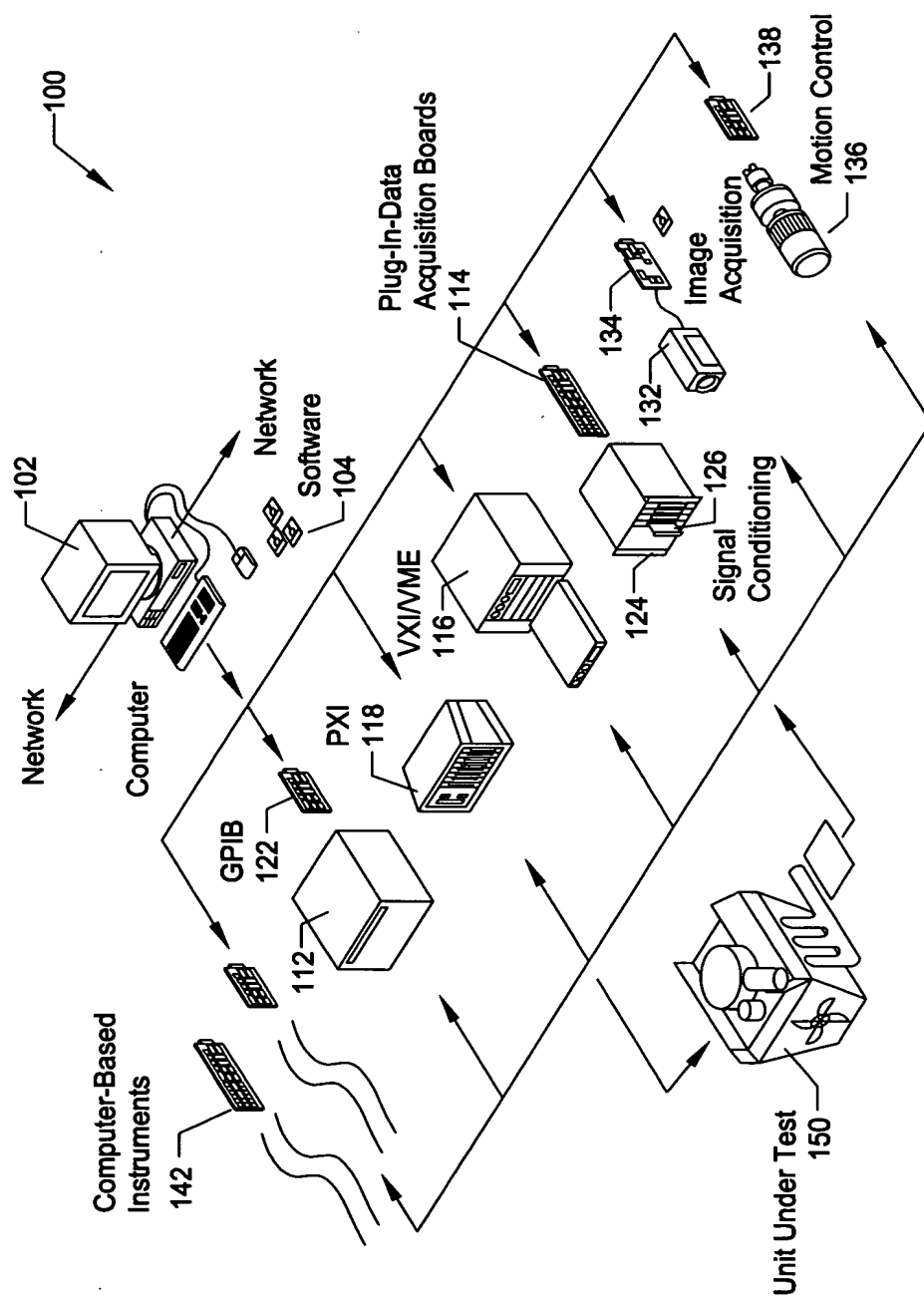


FIG.1



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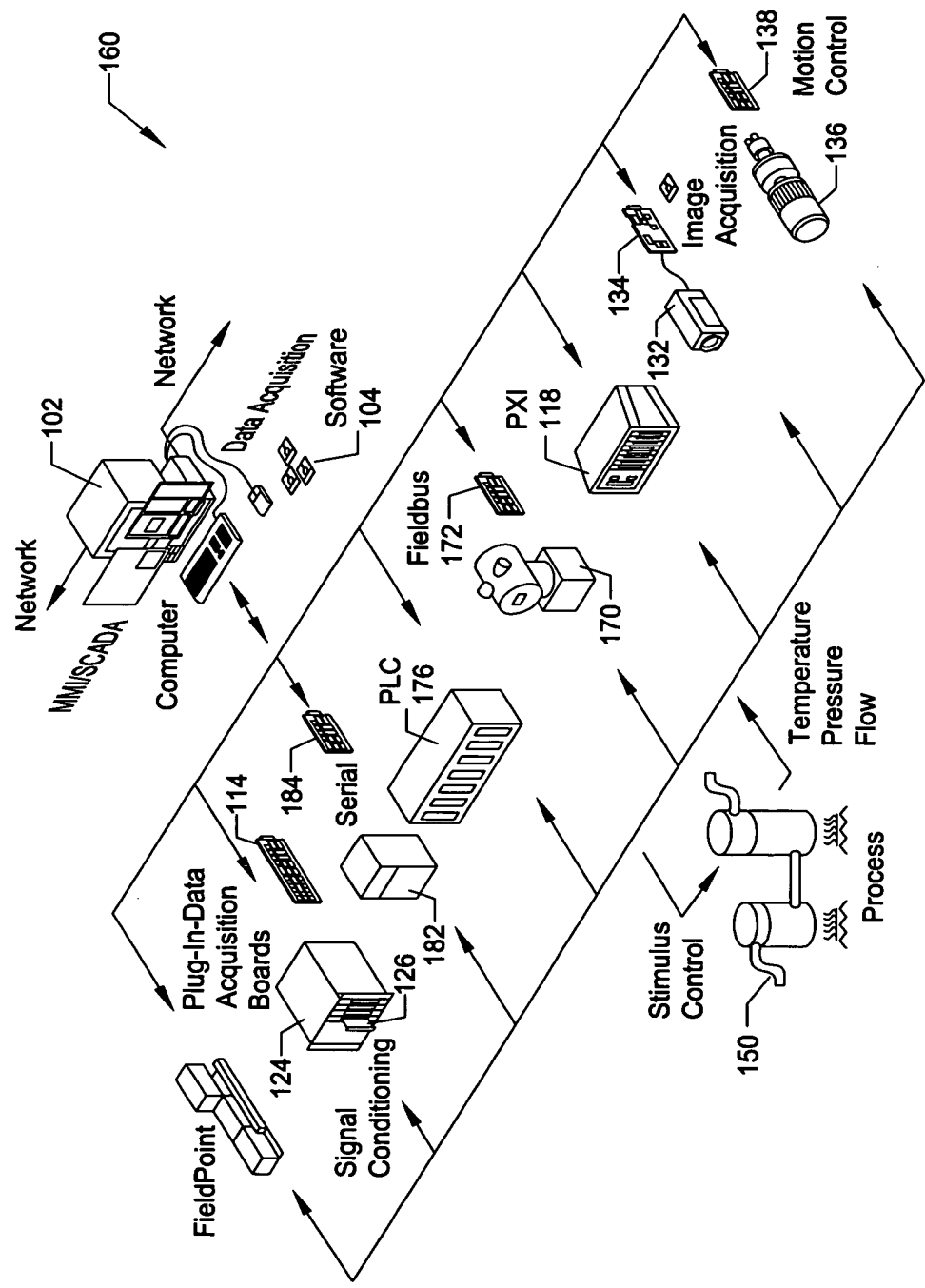


FIG. 2B

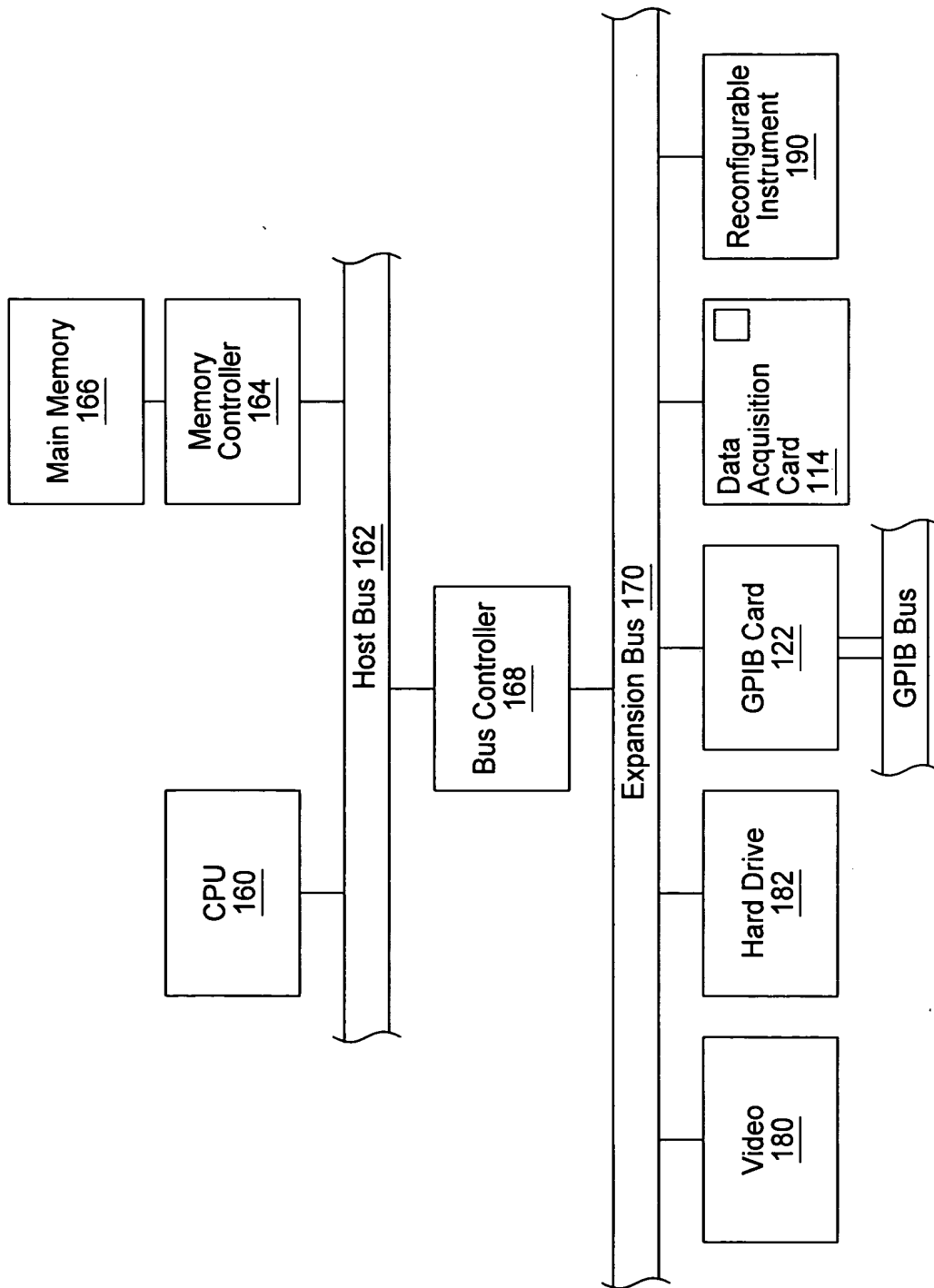


FIG. 3

Developer creates a graphical program generation (GPG) program, wherein the GPG program is operable to generate a plurality of graphical programs, based on received information

200

Specify program information, e.g., in response to user input, wherein the program information specifies desired functionality to be implemented in a graphical program

204

execute graphical program generation (GPG) program

206

GPG program receives information specifying functionality for a graphical program (or graphical program portion)

208

GPG program programmatically generates a graphical program (or graphical program portion) to implement the specified functionality

210

FIG. 4

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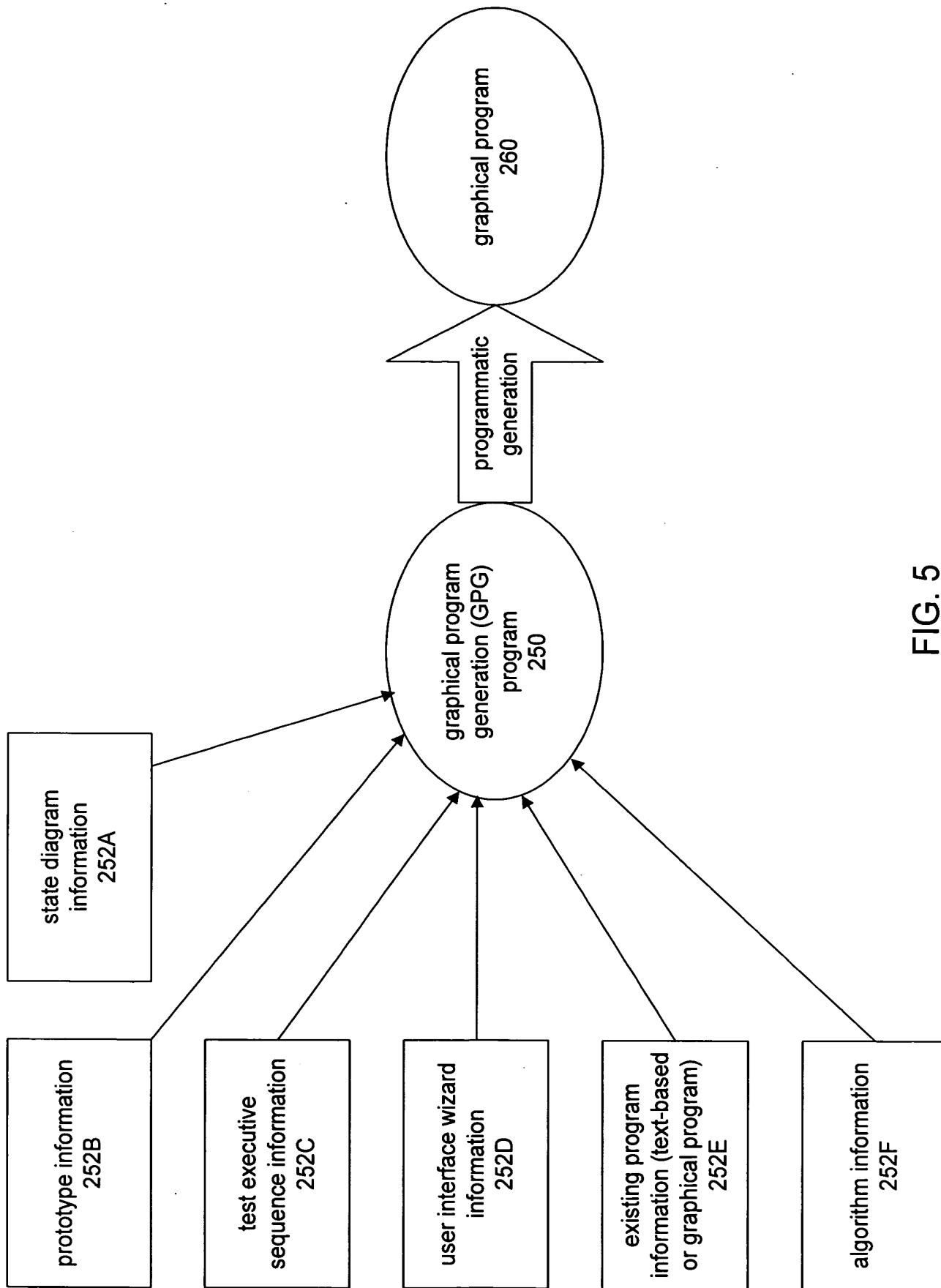


FIG. 5

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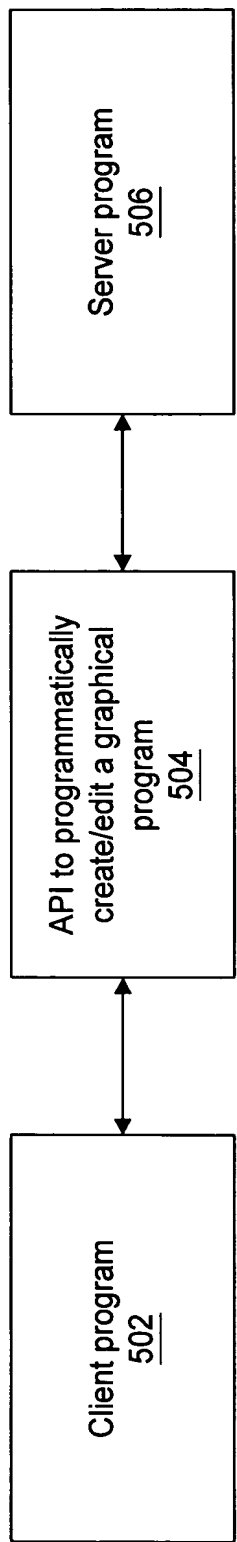


FIG. 6

The diagram shows a control loop block. On the left, there are three inputs: "machine name" (dashed line), "port number" (solid line), and "error in (no error)" (dashed line). On the right, there are two outputs: "application reference" (solid line) and "error out" (dashed line). Inside the block, the inputs "machine name" and "port number" are connected to a block labeled "VI". The input "error in (no error)" is connected to a block labeled "O". The outputs of "VI" and "O" are connected to a summing junction (a triangle with a plus sign). The output of the summing junction is connected to the "error out" output. The "application reference" output is connected to the "VI" block.

FIG. 8

Diagram illustrating the VI (Value Input) block structure and its inputs/outputs:

- Inputs (Left):**
 - type specifier (solid line)
 - application reference (local) (solid line)
 - VI name or path (dashed line)
 - error in (no error) (thick dashed line)
 - password (dashed line)
- Block Label:** VI
- Block Symbol:** O
- Internal Structure:** A 2x2 grid of squares, with the top-left square filled black.
- Outputs (Right):**
 - application reference (solid line)
 - error out (thick dashed line)

FIG. 8

FIG. 9

FIG. 10

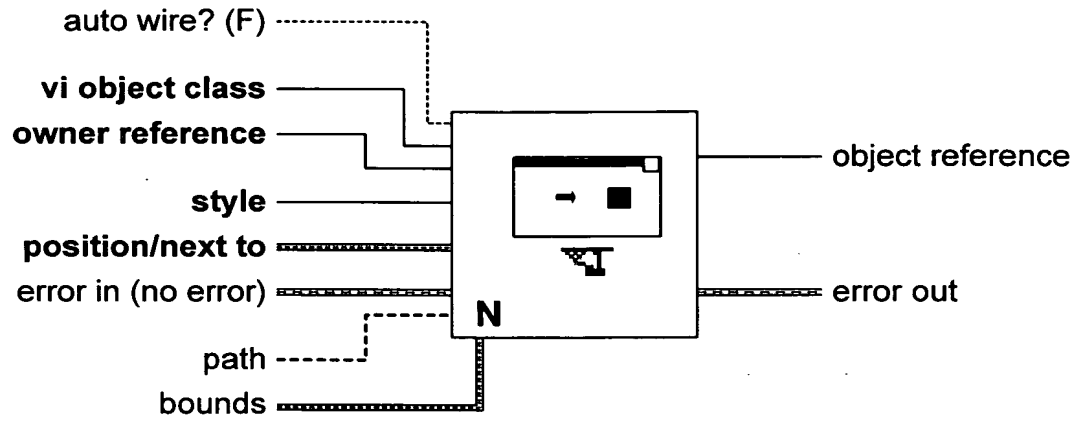


FIG. 11

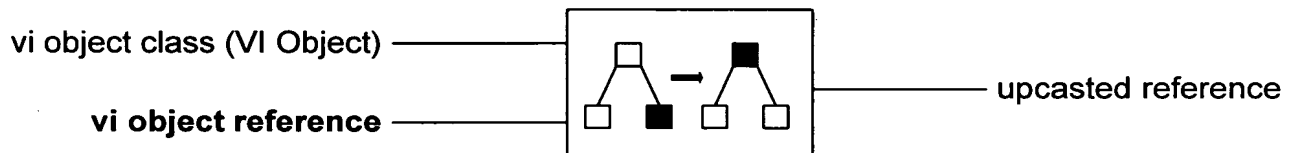


FIG. 12

Downcast Reference Node

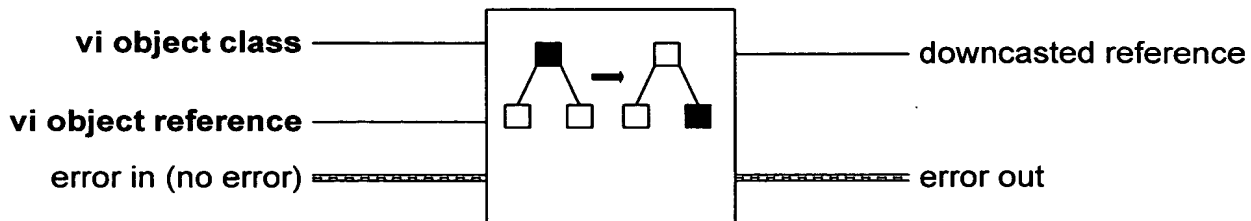


FIG. 13

Close Application or VI Reference Node

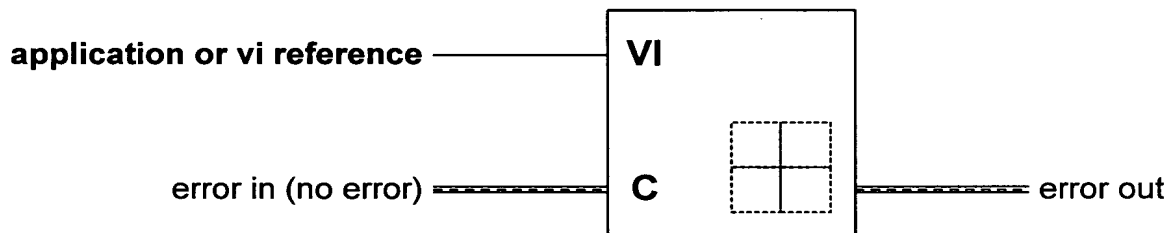


FIG. 14

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Call By Reference Node

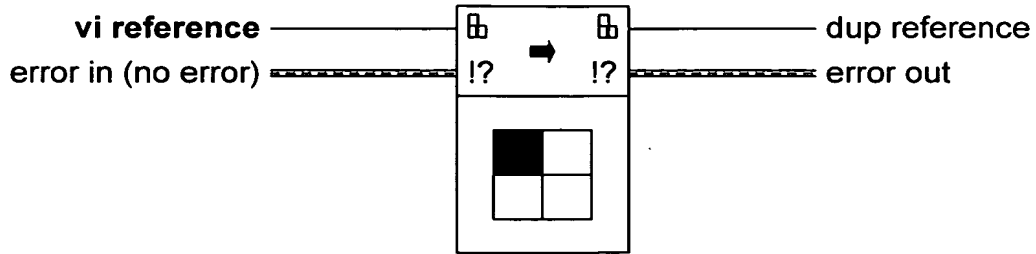


FIG. 15

Property Node

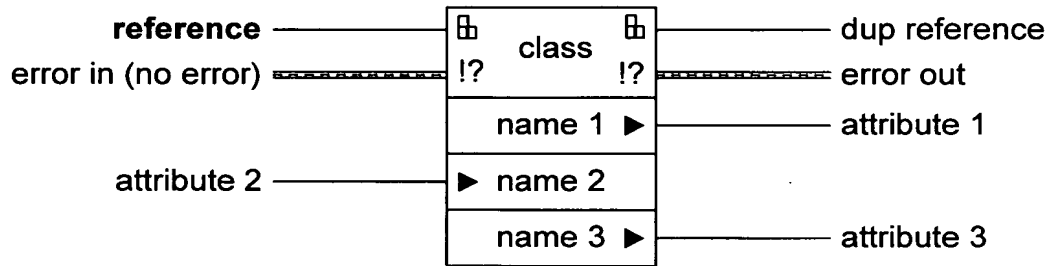


FIG. 16

Invoke Node

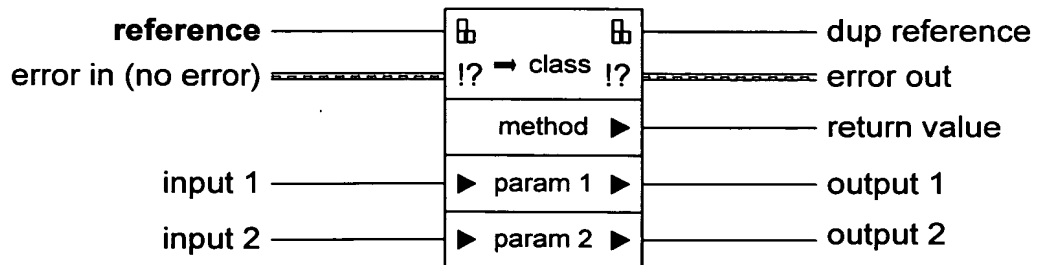


FIG. 17

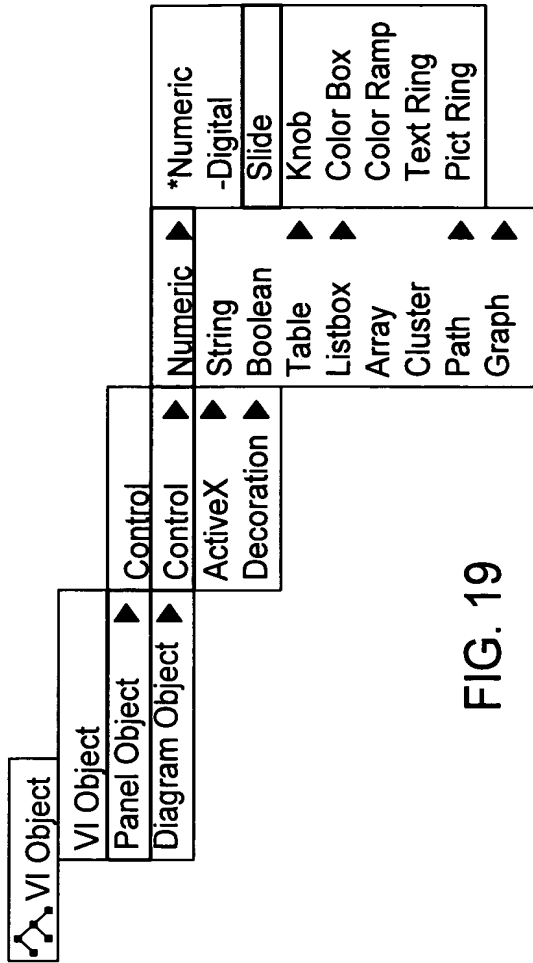


FIG. 18

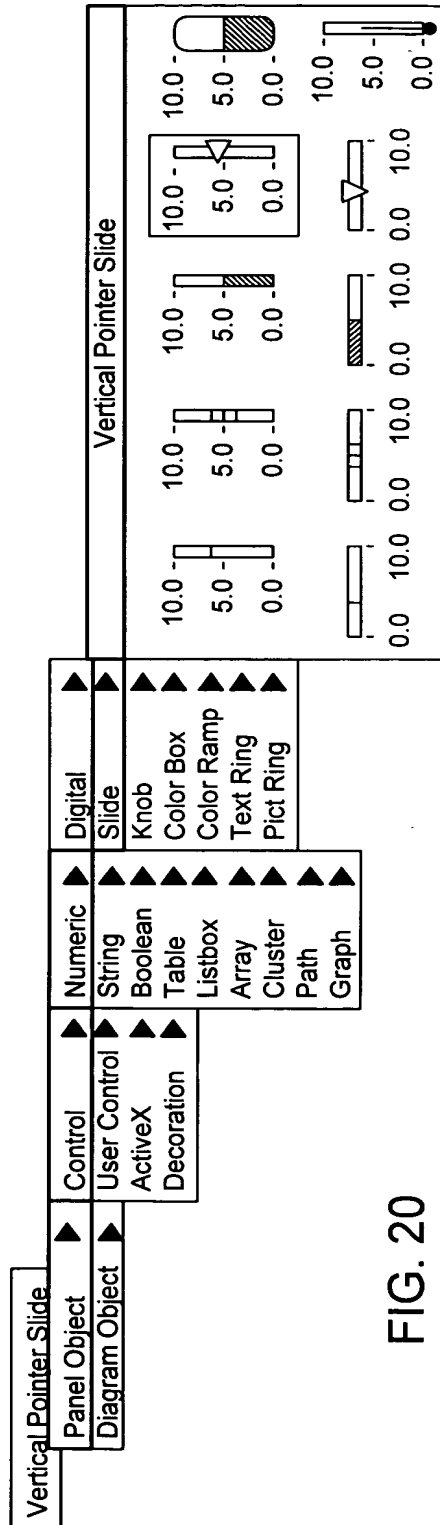


FIG. 20

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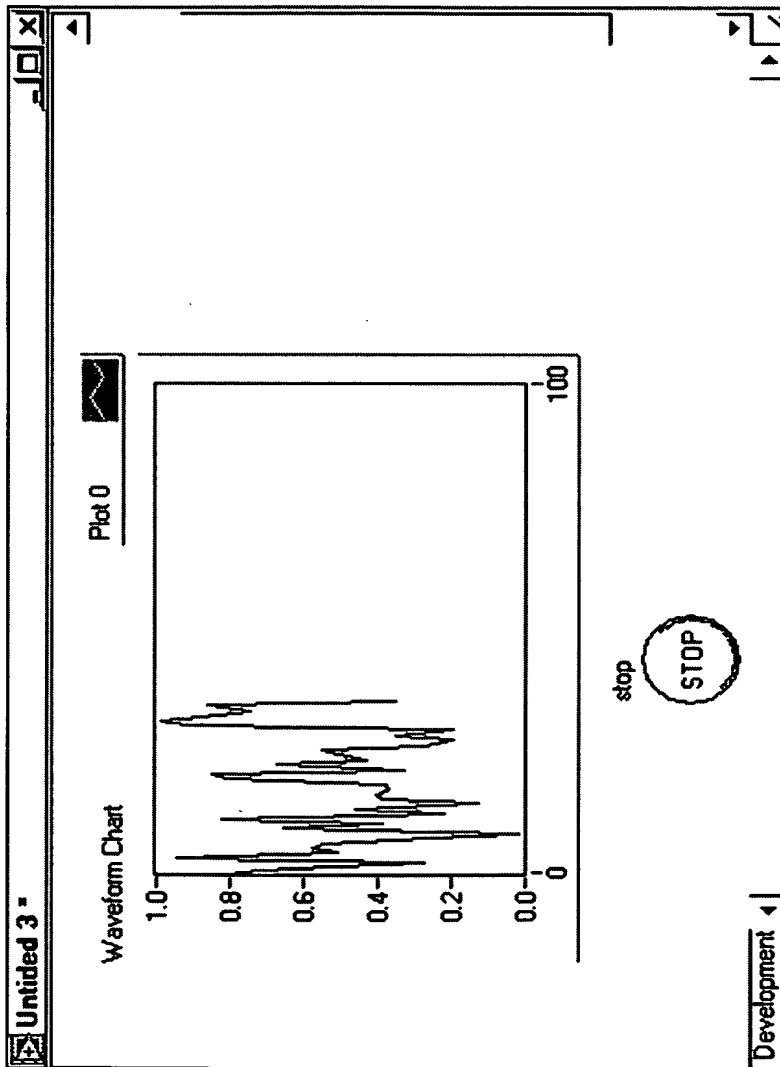


FIG. 21

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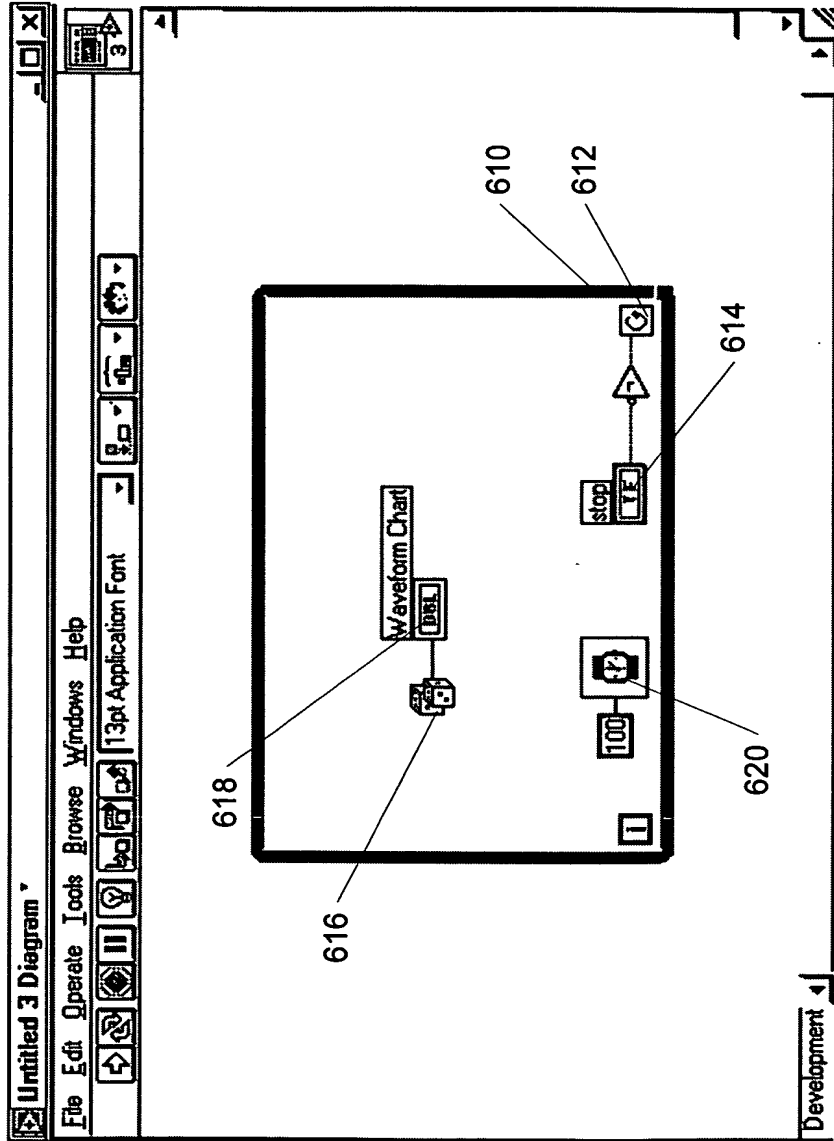


FIG. 22

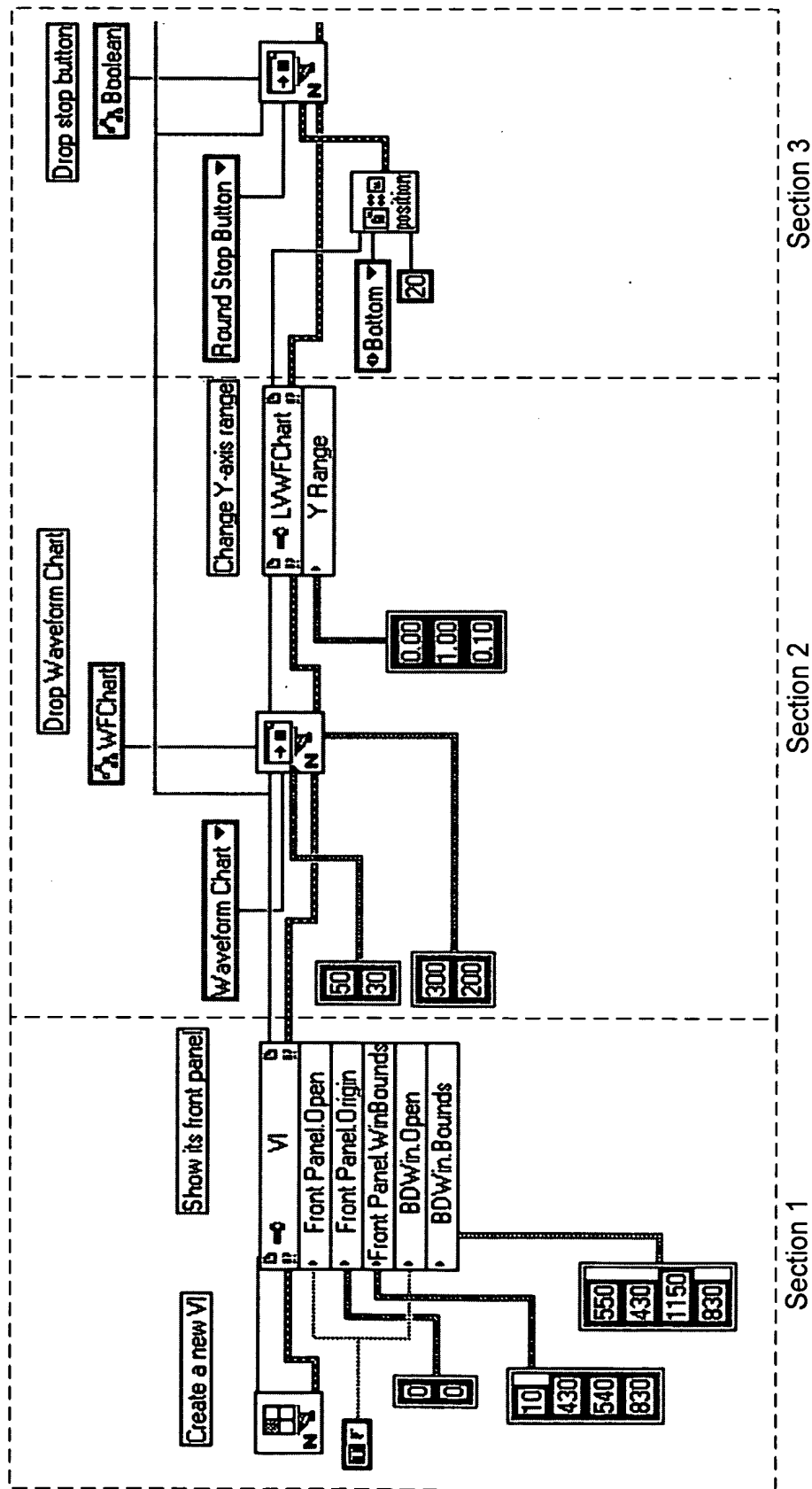
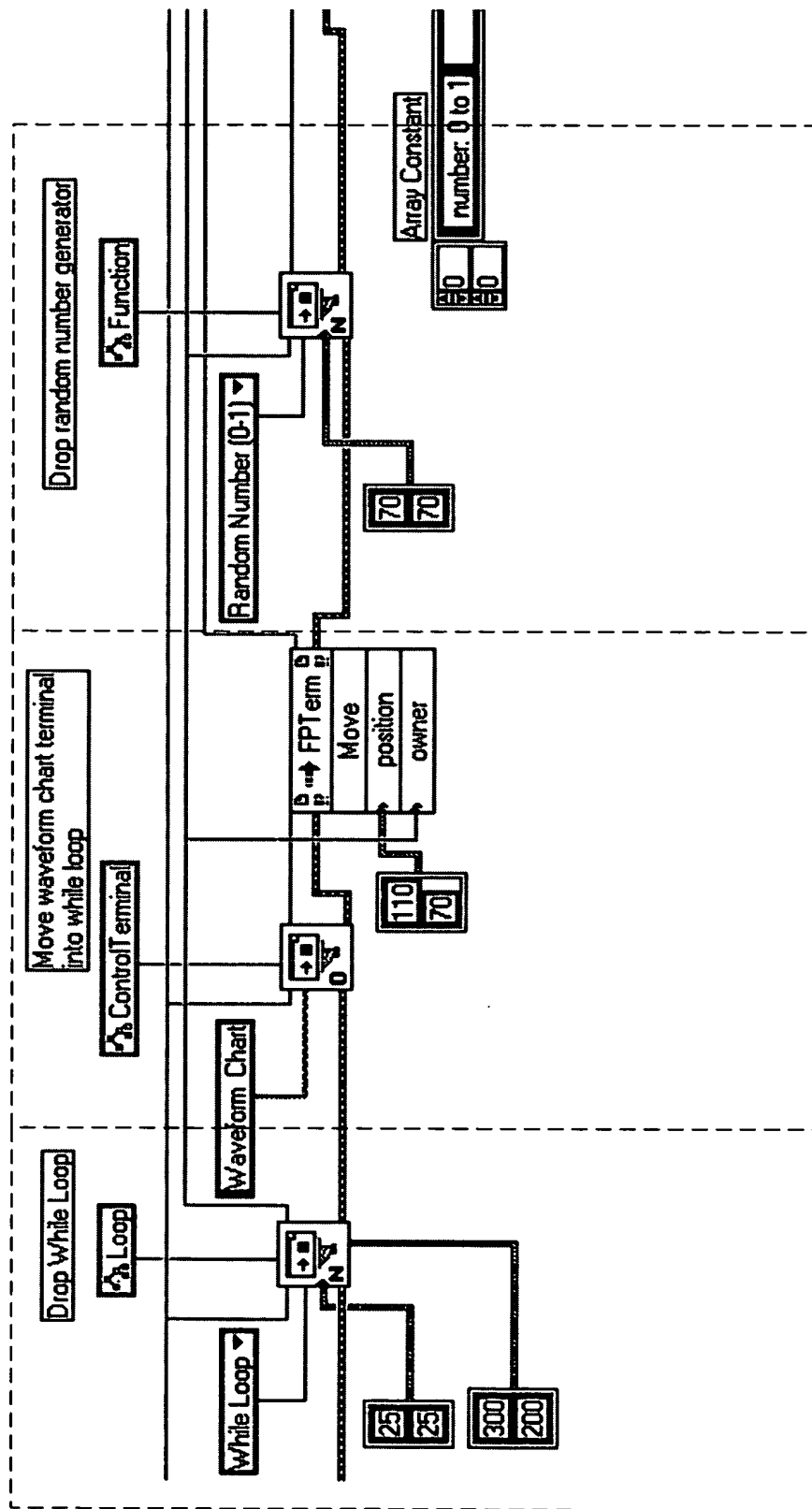


FIG. 23A

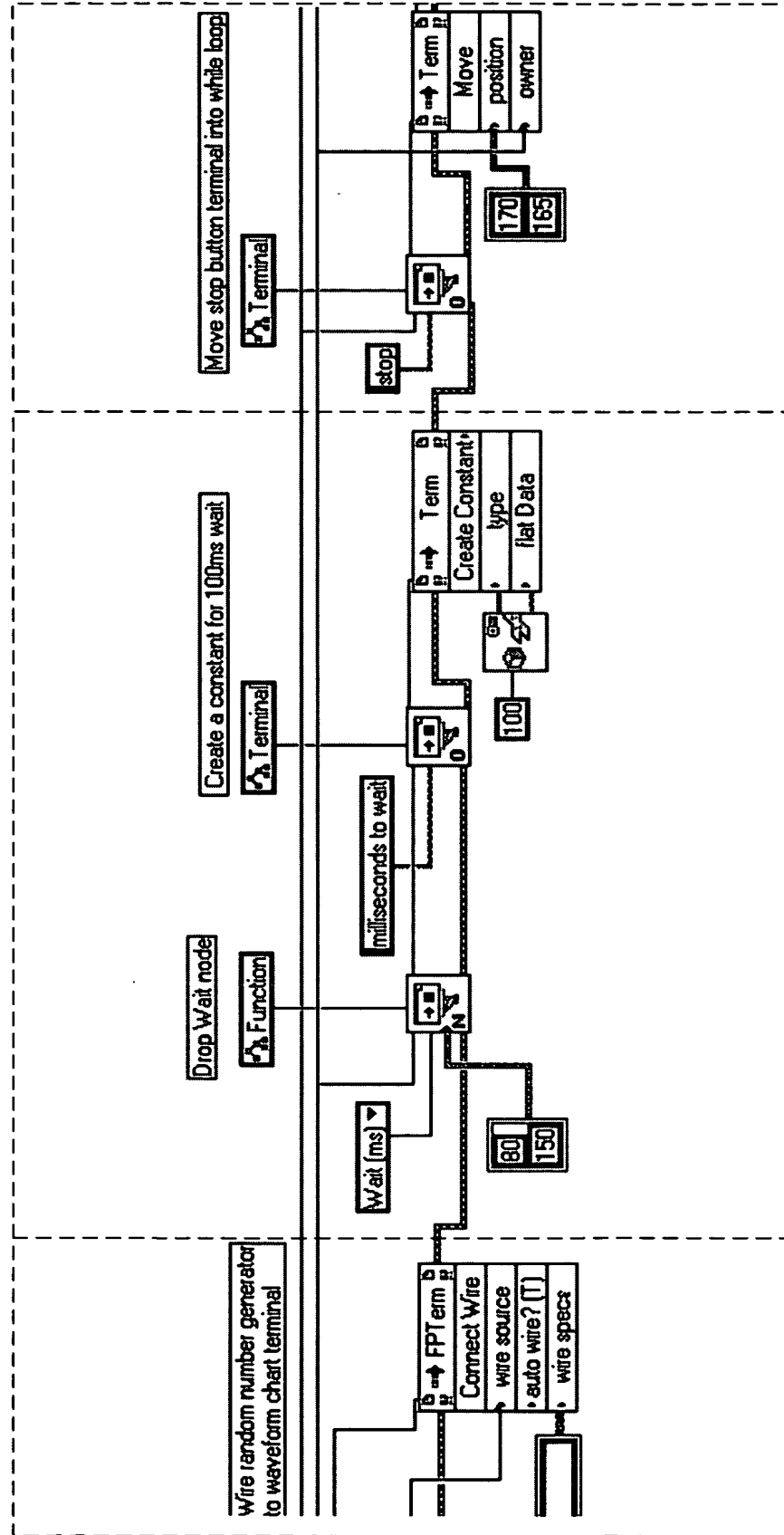


Section 6

Section 5

Section 4

FIG.23B
(Continued)



Section 7

Section 8

Section 9

FIG.23C
(Continued)

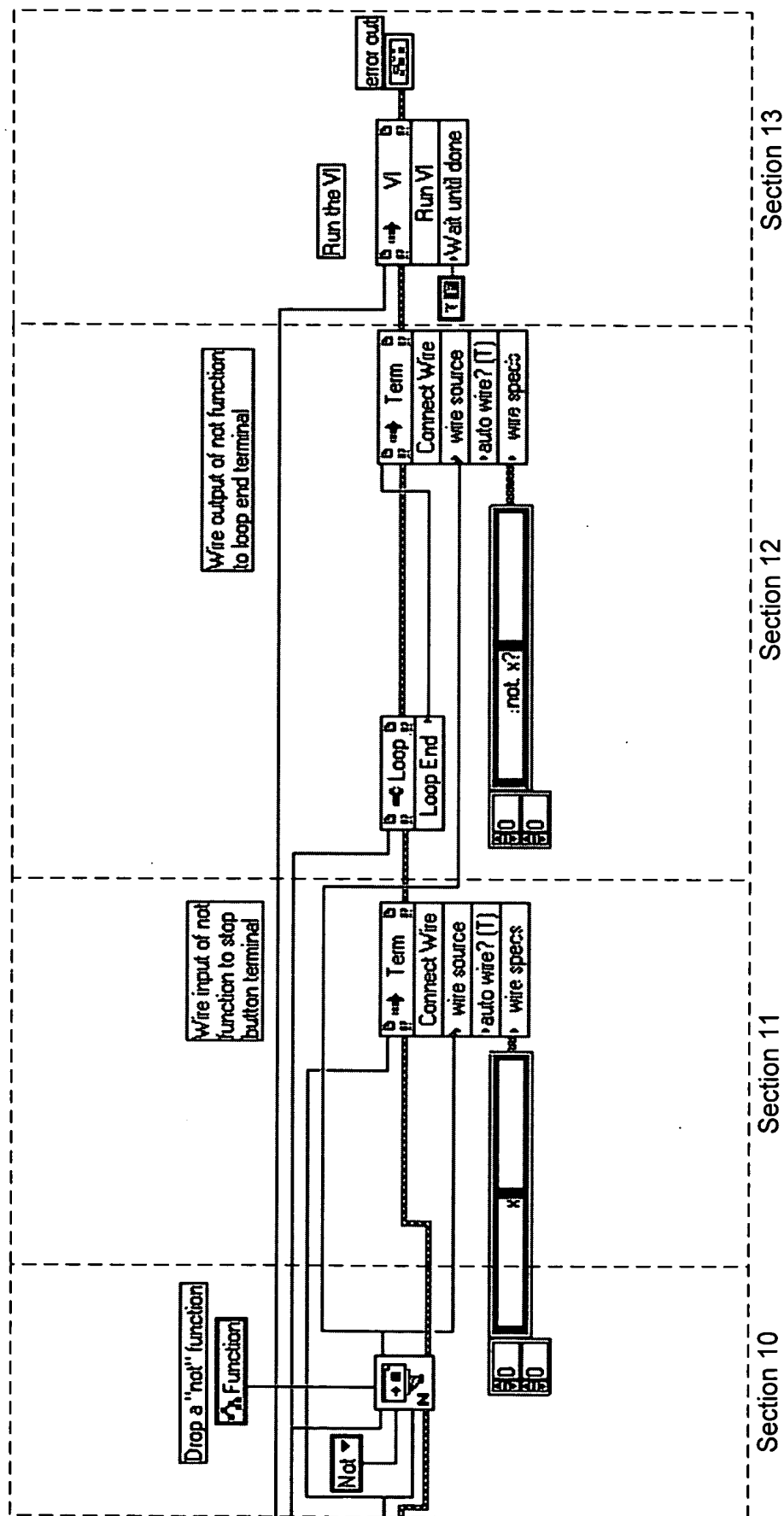


FIG. 23D
(Continued)